

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Staszewski et al  
Serial No.: not assigned  
Filing Date: November 30, 2001  
Group Art Unit: not assigned  
Examiner: not assigned  
Title: FREQUENCY SYNTHESIZER WITH PHASE RESTART

Honorable Commissioner of  
Patent and Trademarks  
Washington, D.C. 20231

Express Mail Certificate No: EK338727595US

LETTER TO THE OFFICIAL DRAFTSPERSON

Dear Sir:

Enclosed herewith are the formal drawings for the subject application, for replacement of those filed with the enclosed application.

Respectfully submitted,  
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Attorneys for Applicant

  
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November 30, 2001

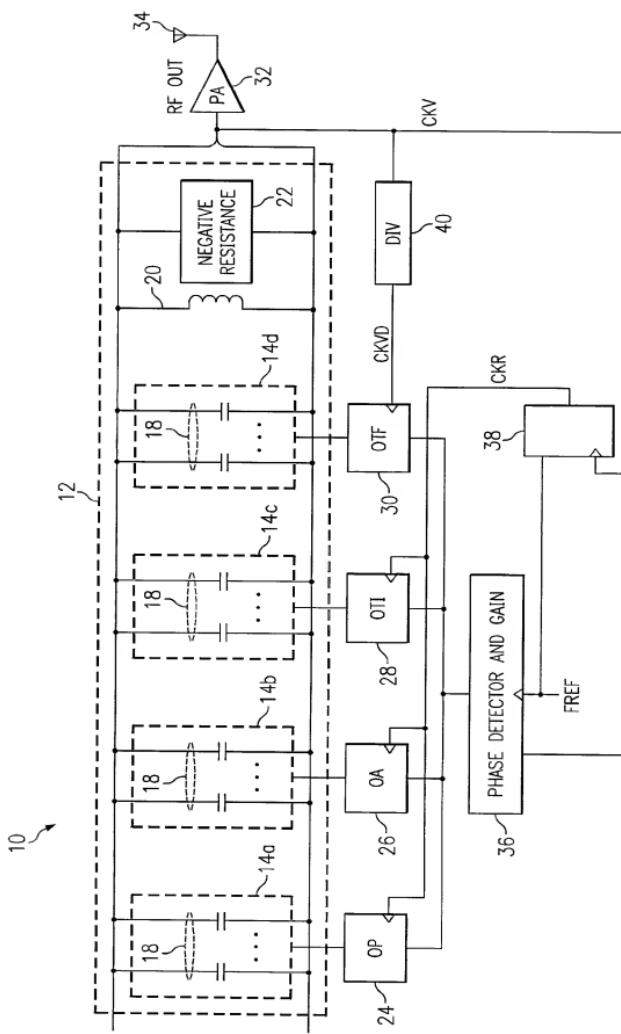


FIG. 1

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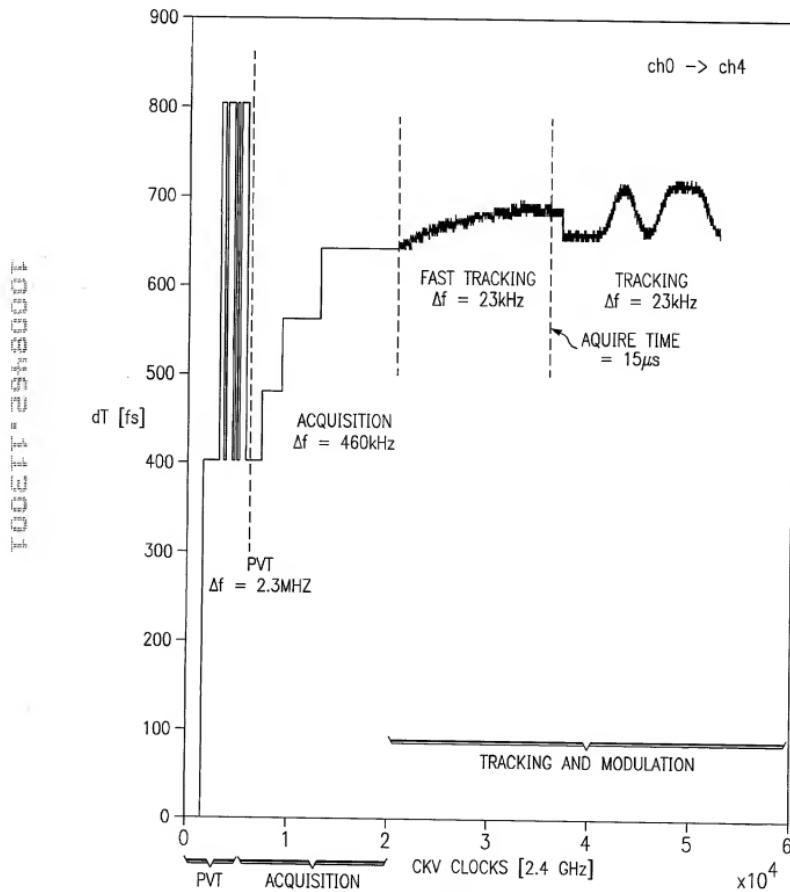


FIG. 2

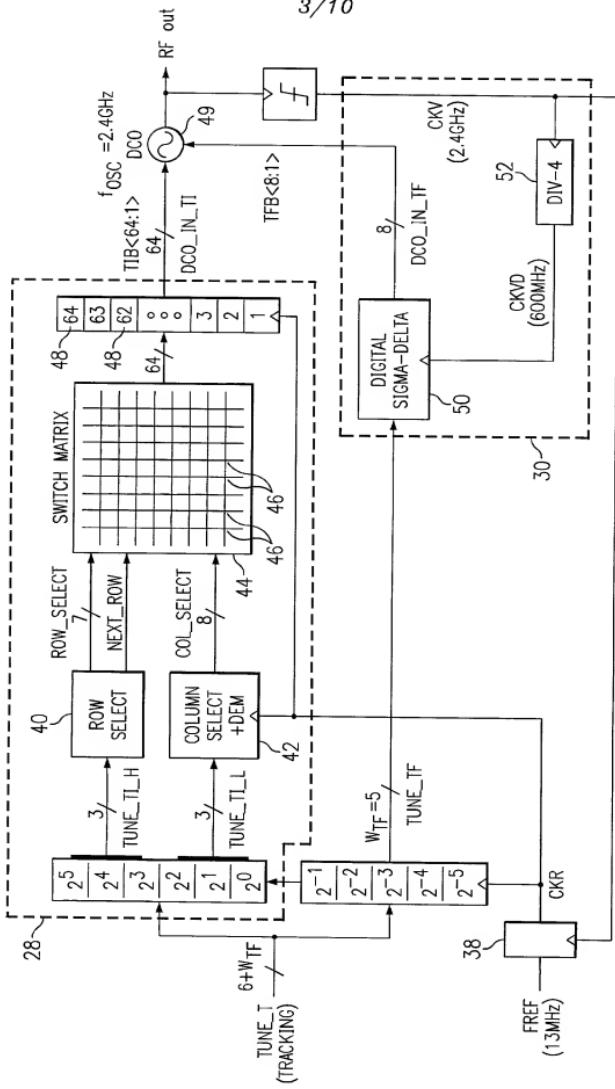
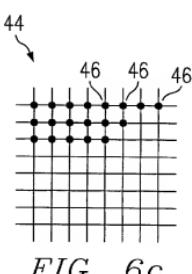
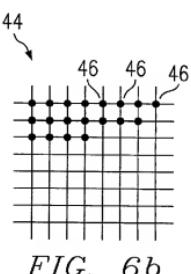
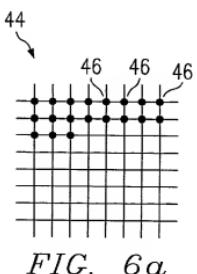
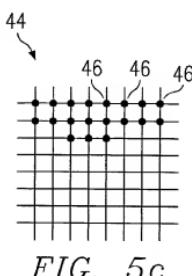
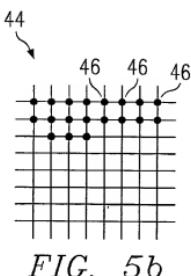
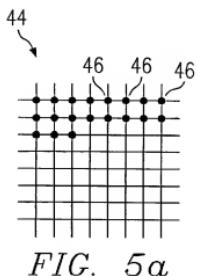
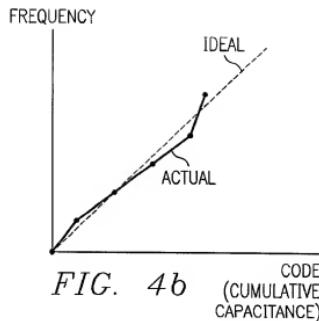
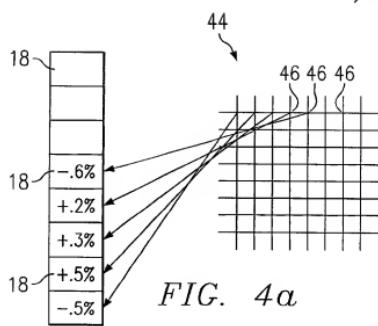


FIG. 3



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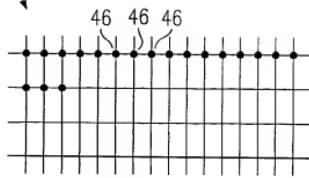


FIG. 7a

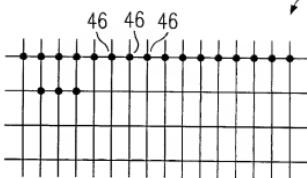


FIG. 7b

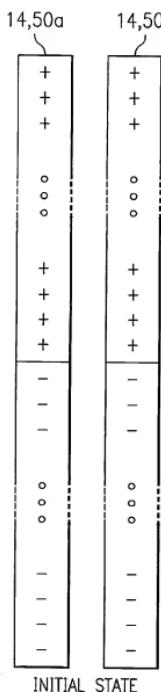


FIG. 9a

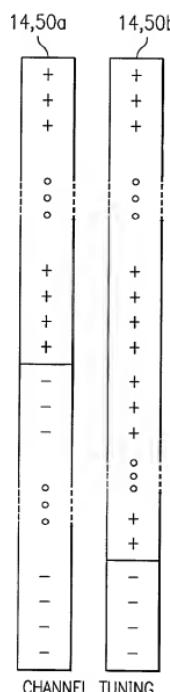


FIG. 9b

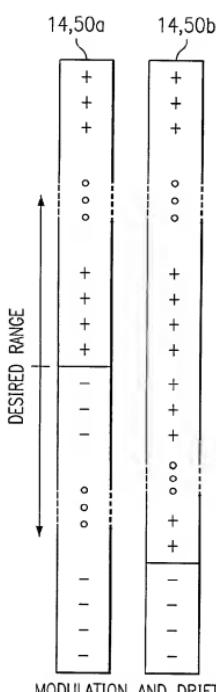
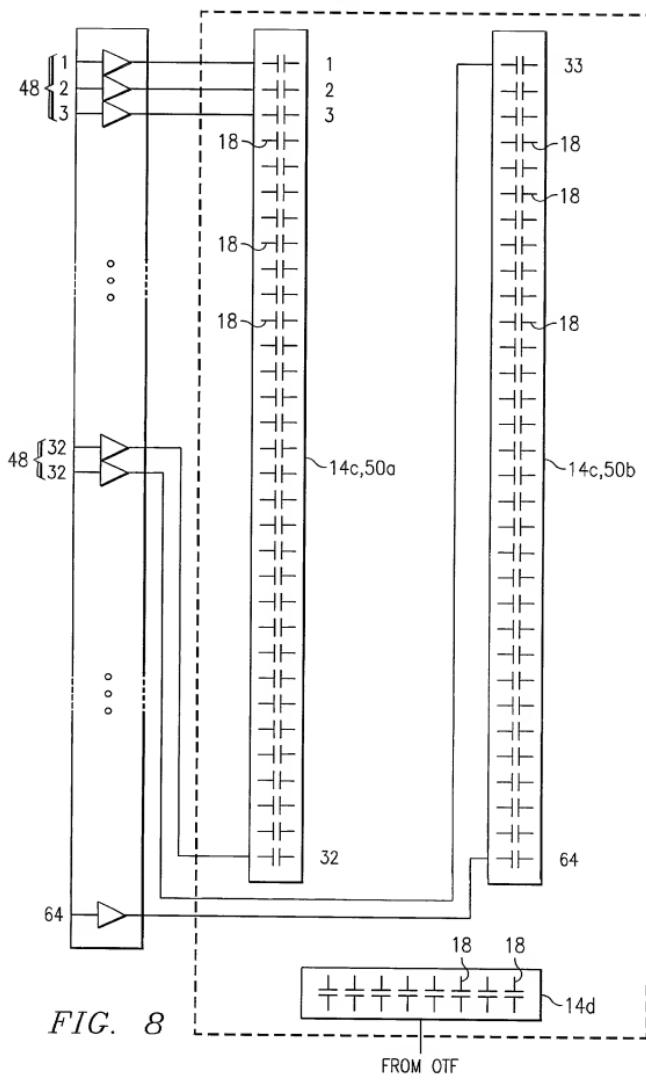


FIG. 9c



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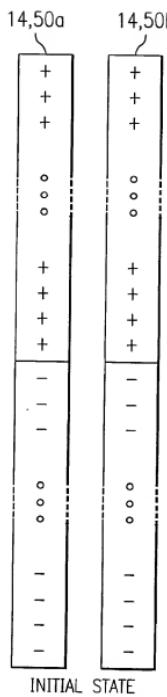


FIG. 10a

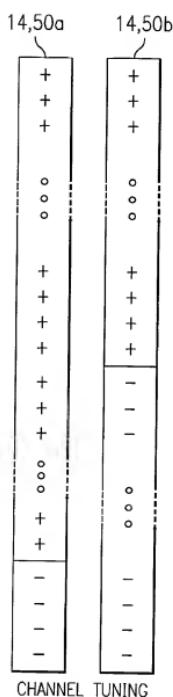


FIG. 10b

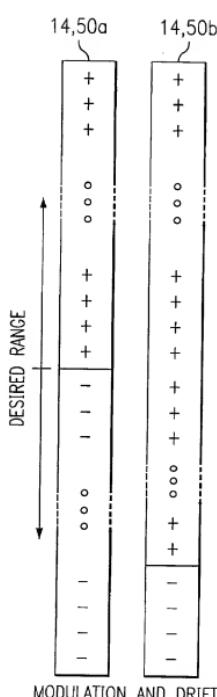


FIG. 10c

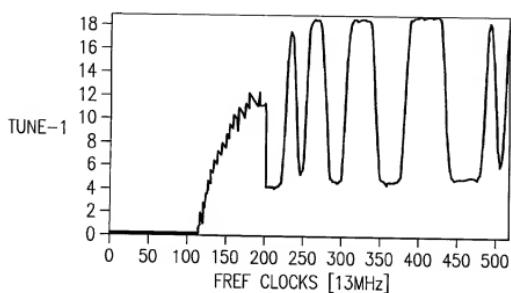


FIG. 11a

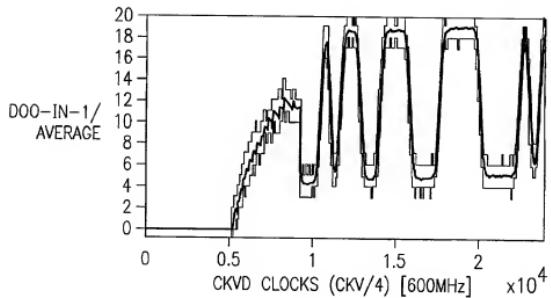
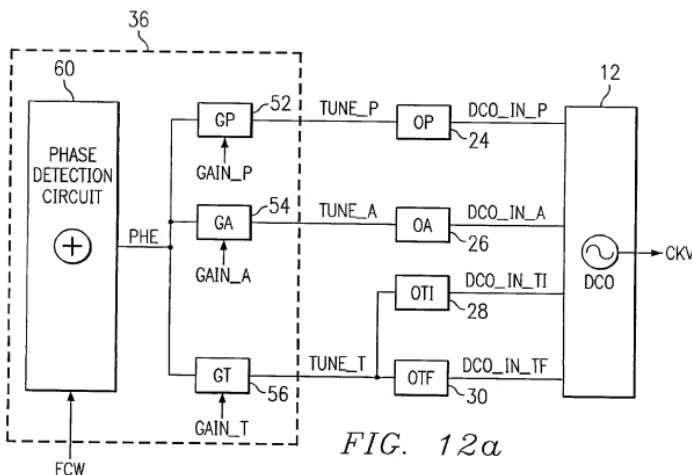


FIG. 11b



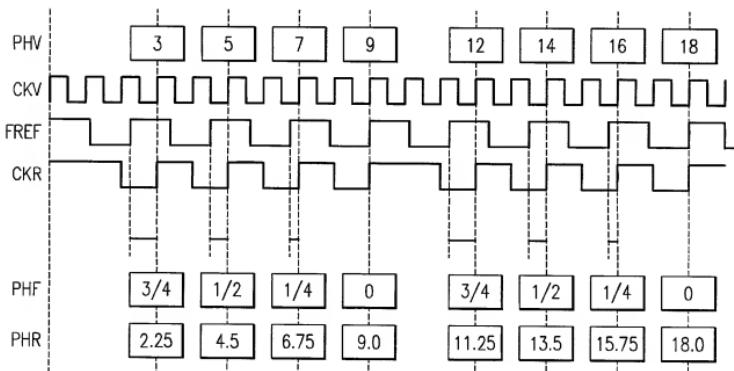
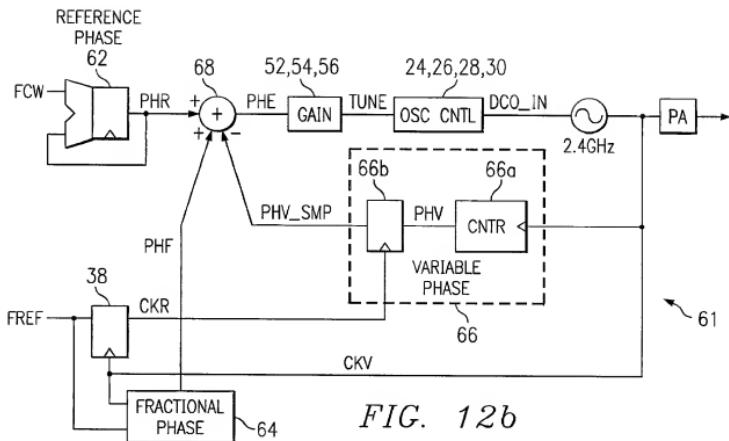


FIG. 12c

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